

Applicants: Walter Fix et al.
Serial No.: 10/562,869
Filed: April 7, 2006
For: Logic Gate with a Potential-Free Gate Electrode for Organic Integrated Circuits
Examiner: Eva Y. Montalvo Art Unit: 2814
Attorney Dkt: 411000-144 Customer No. 27162

RESPONSE UNDER 37 CFR 1.112

MS Amendment
Commissioner for Patents
Box 1450
Alexandria, VA 22313-1450

This paper is in response to a non-final Office Action dated Dec. 28, 2009 having a one month extended due date for response set to expire on April 28, 2010. The Commissioner is authorized to charge deposit account 03 0678 for the \$130 fee due for one month extension of time or any other fee that might be due for this paper or credit any over payments. The Action originally was Final. However, applicants petitioned to withdraw the finality. This petition was granted in the Interview Summary paper dated Mar. 22, 2010.

IN THE CLAIMS

Amend the claims as shown starting on page 2 of this paper.

Remarks begin on page 4.